



App No.: 09/359,056

Docket No.: M4065.0767/P767

Inventor: Barmak Mansoorian

Title: HIGH RESOLUTION CMOS CIRCUIT USING A
MATCHED IMPEDANCE OUTPUT TRANSMISSION LINE
REPLACEMENT SHEET

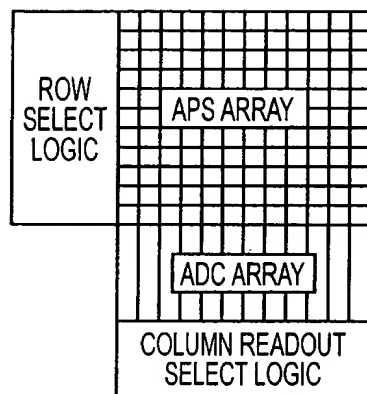


FIG. 1
PRIOR ART

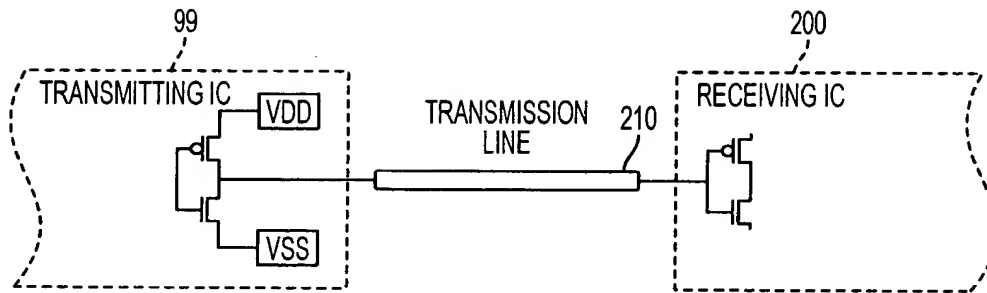


FIG. 2
PRIOR ART

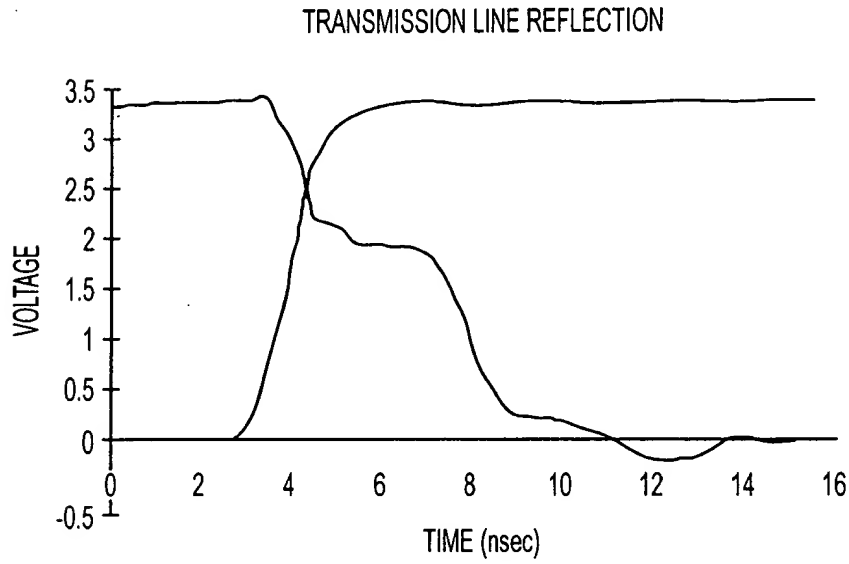


FIG. 3a
PRIOR ART

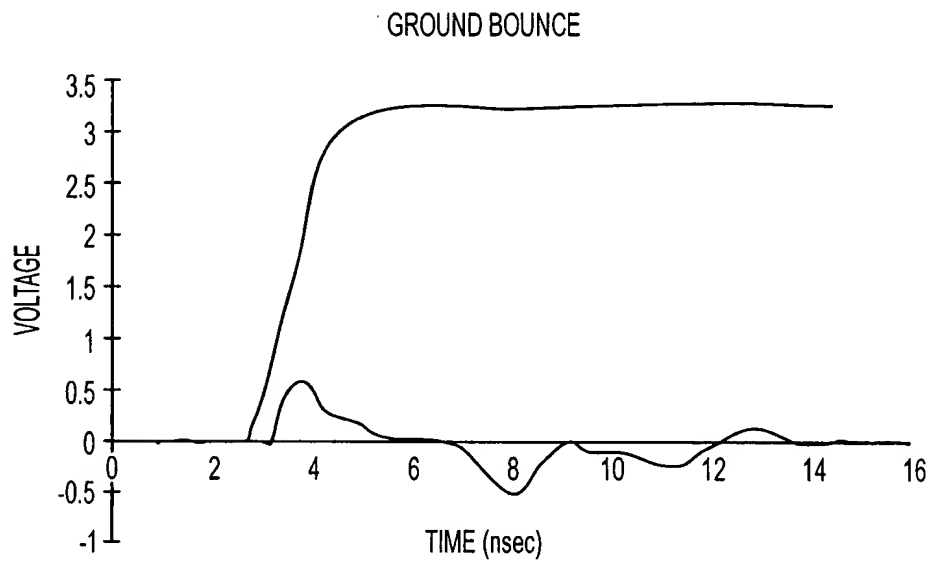


FIG. 3b
PRIOR ART

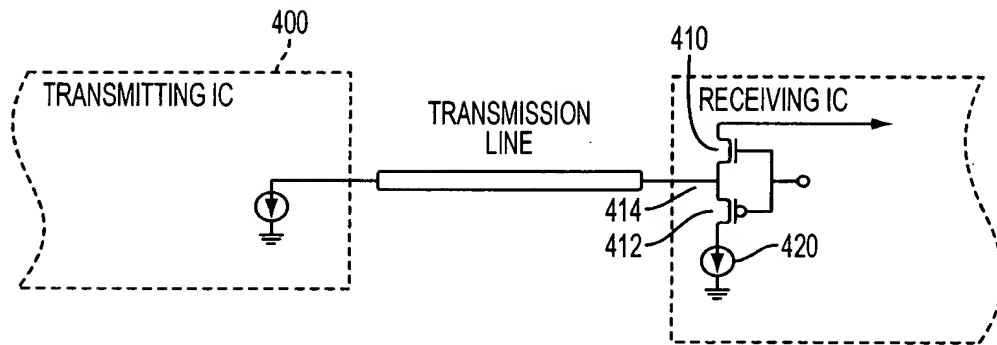


FIG. 4

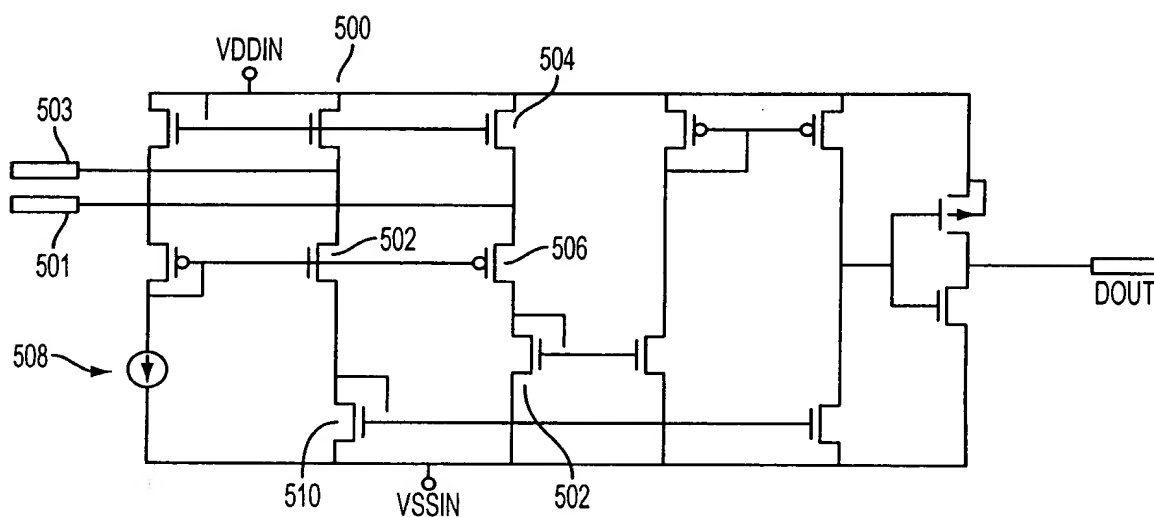


FIG. 5

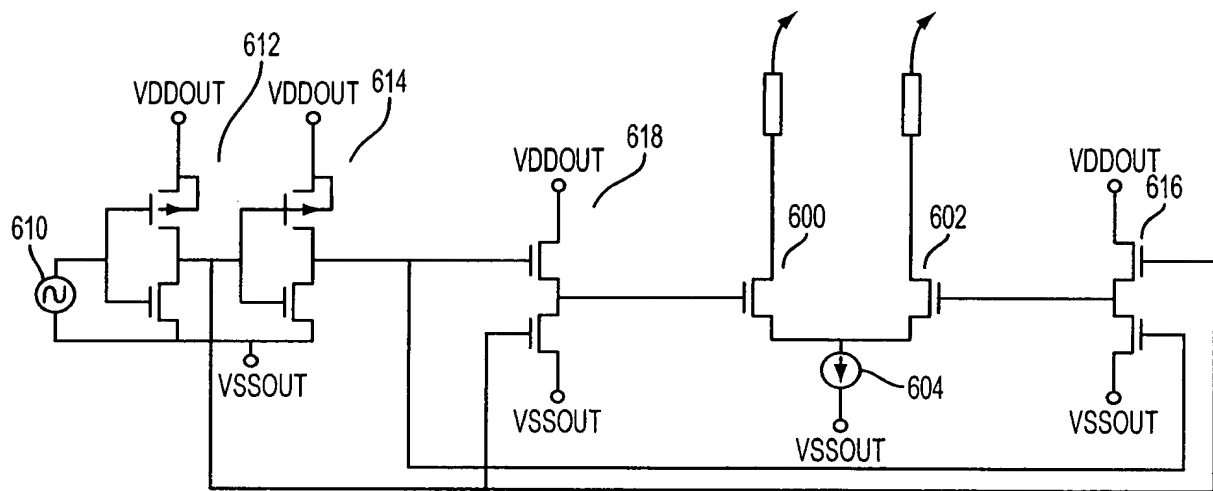


FIG. 6



OUTPUT RESPONSE AT 400 Mbit/sec

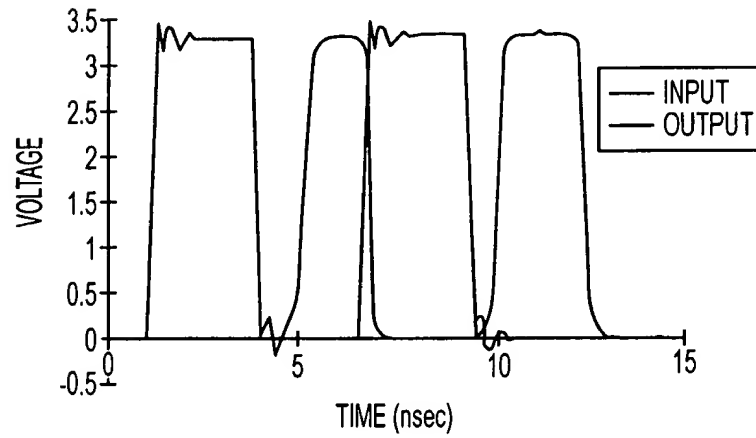


FIG. 7a

GROUND BOUNCE

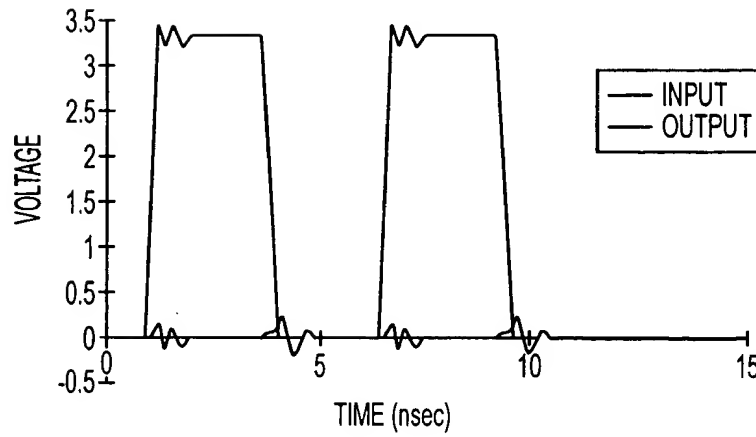


FIG. 7b



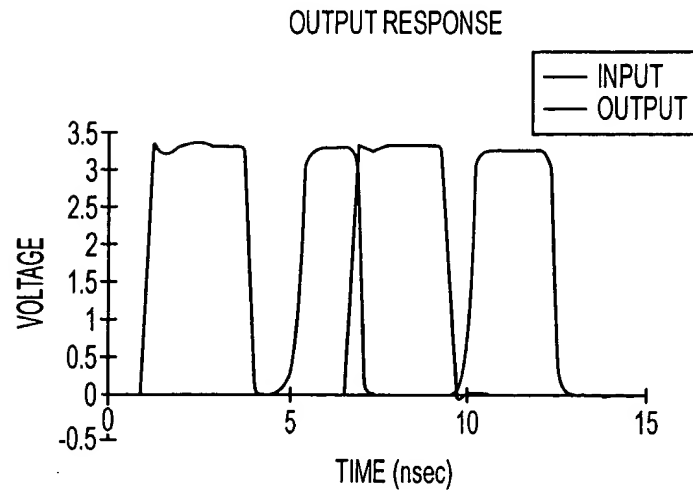


FIG. 9a

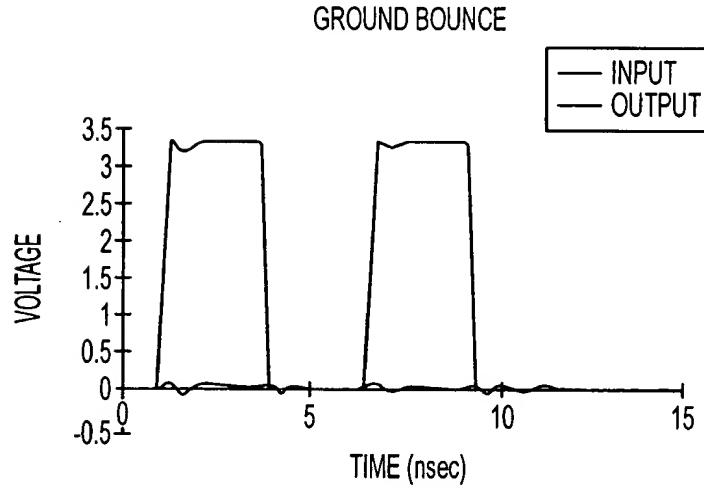


FIG. 9b